

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Weis	Attorney Docket:	01P14627 US01
Filed:	Herewith	Examiner:	TBD
Serial No.:	TBD	Art Unit:	TBD
Title:	Method of Forming an Alignment Mark Structure Using Standard Process Steps for Forming Vertical Gate Transistors		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Applicant respectfully submits this Preliminary Amendment as set forth below: